

FIG. 1A  
PRIOR ART

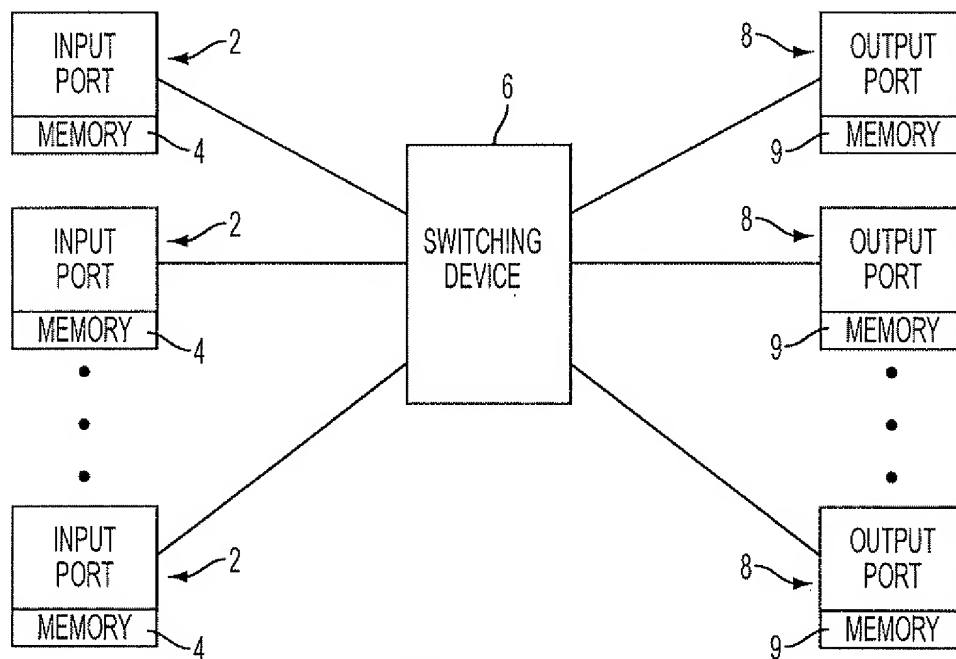


FIG. 1B  
PRIOR ART

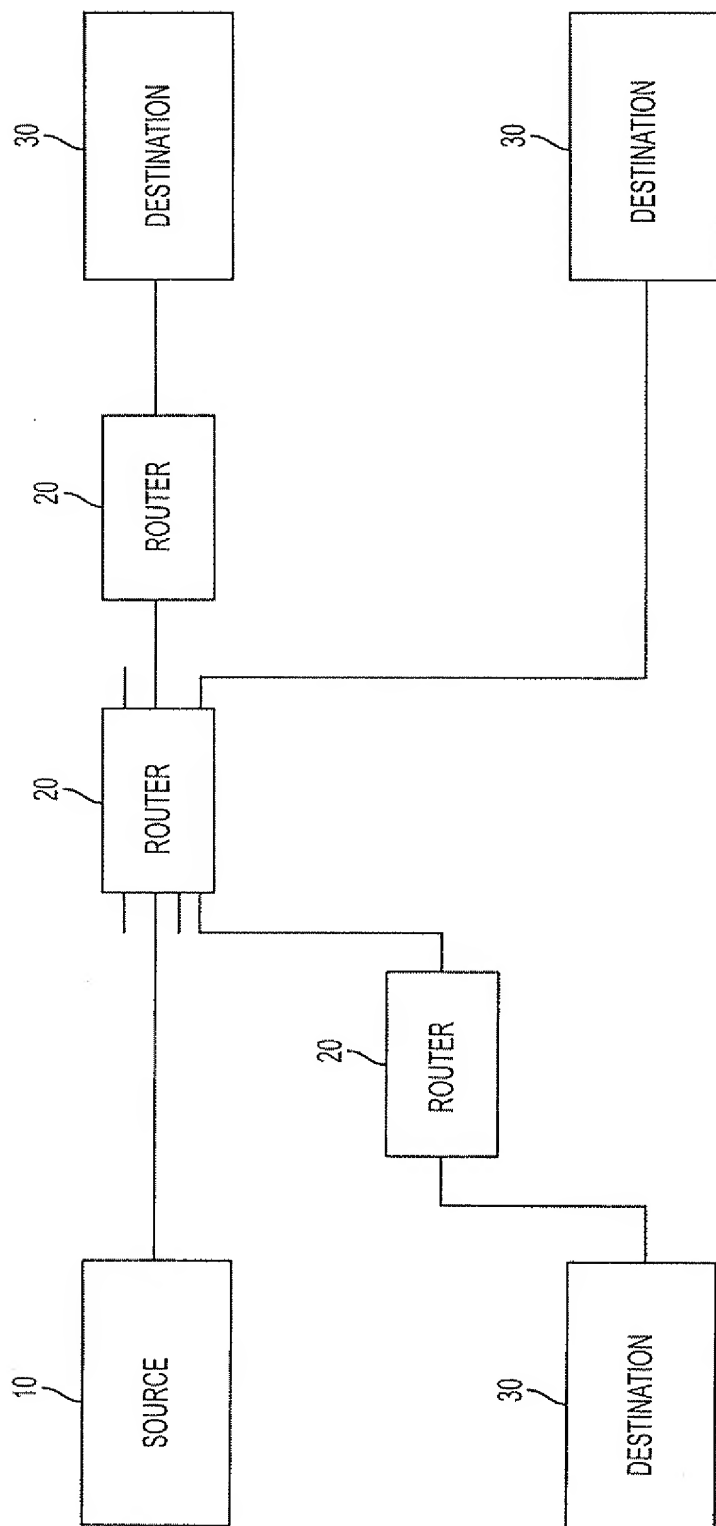


FIG. 2A

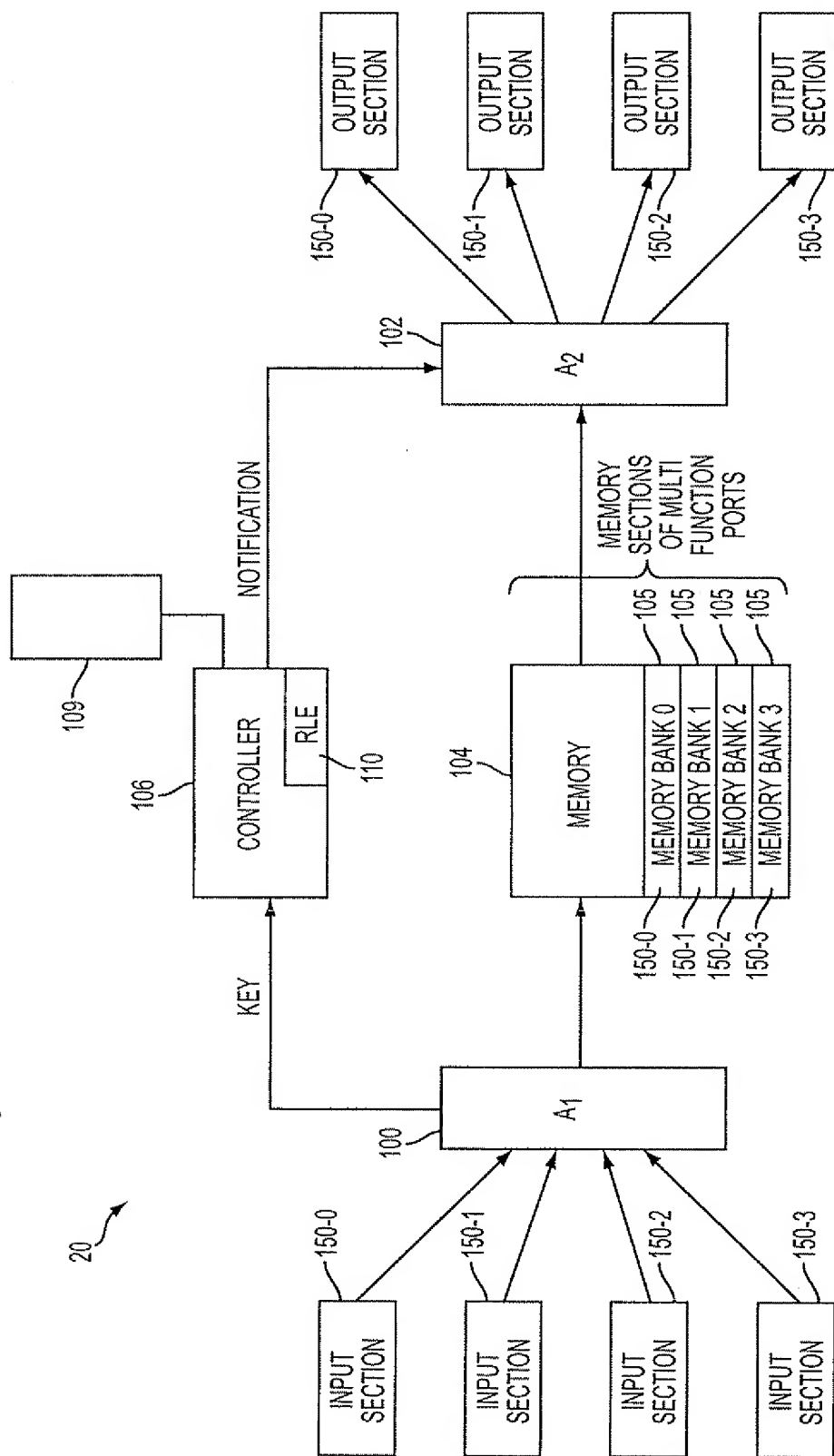


FIG. 2B

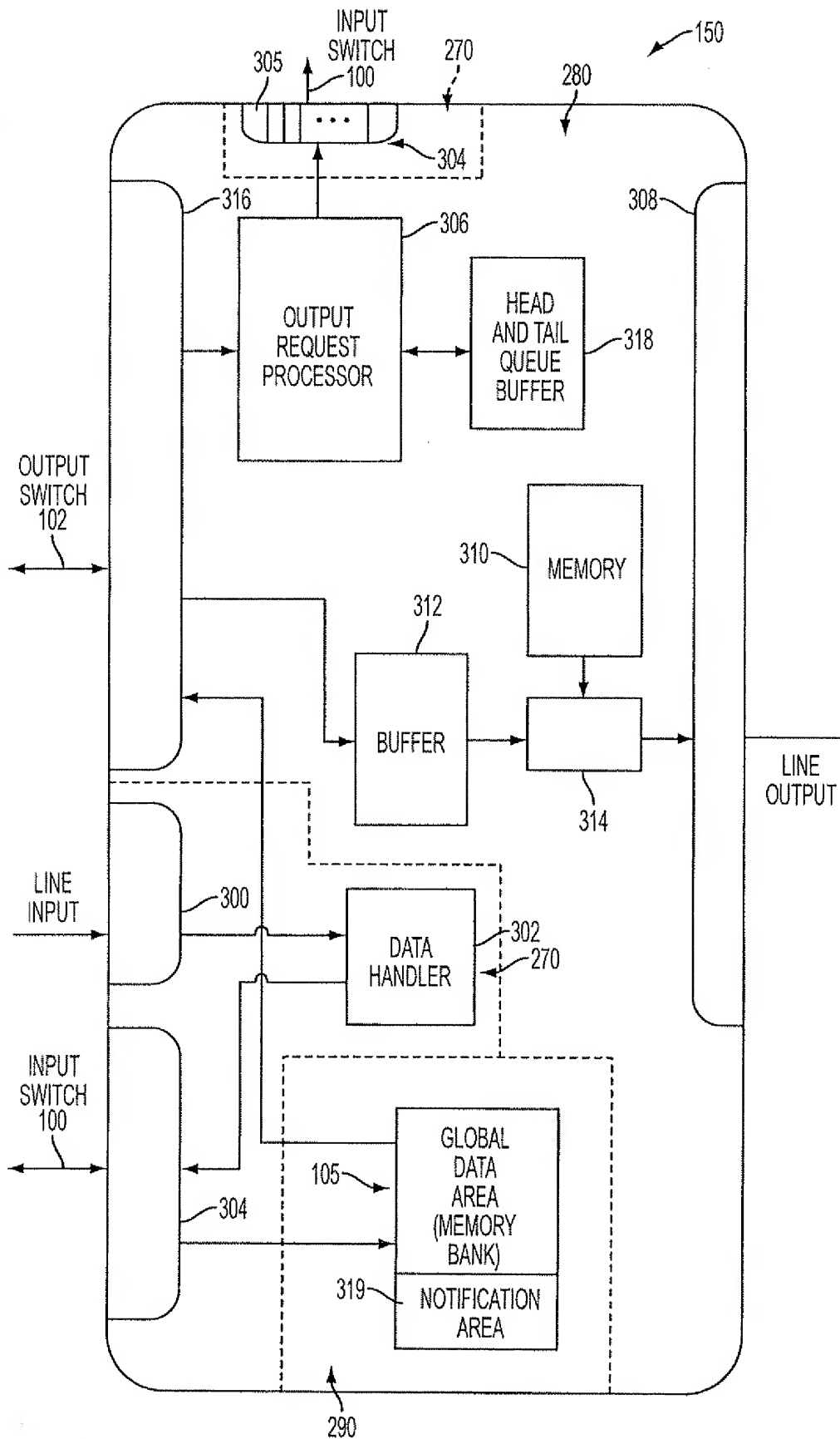


FIG. 3

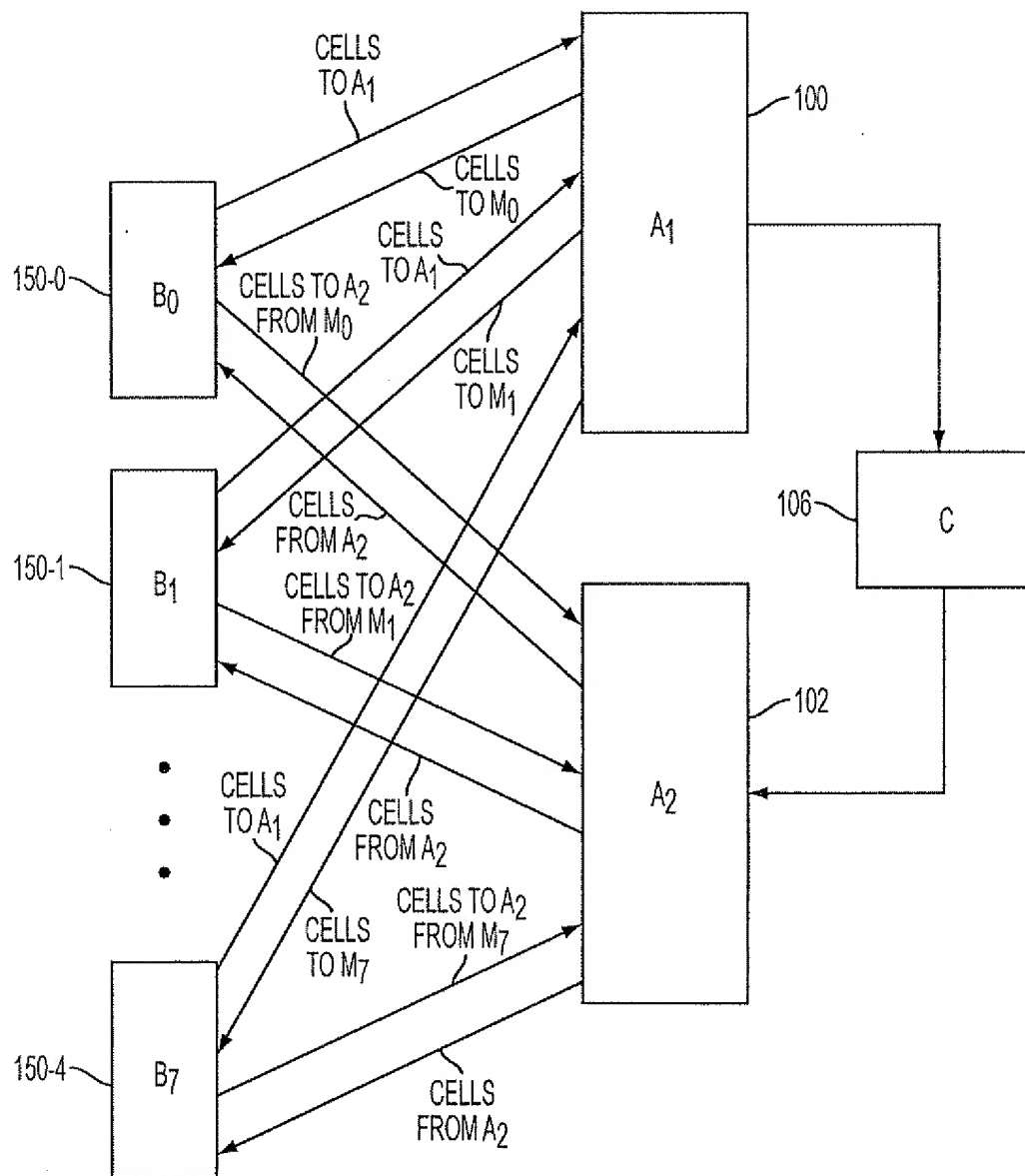


FIG. 4

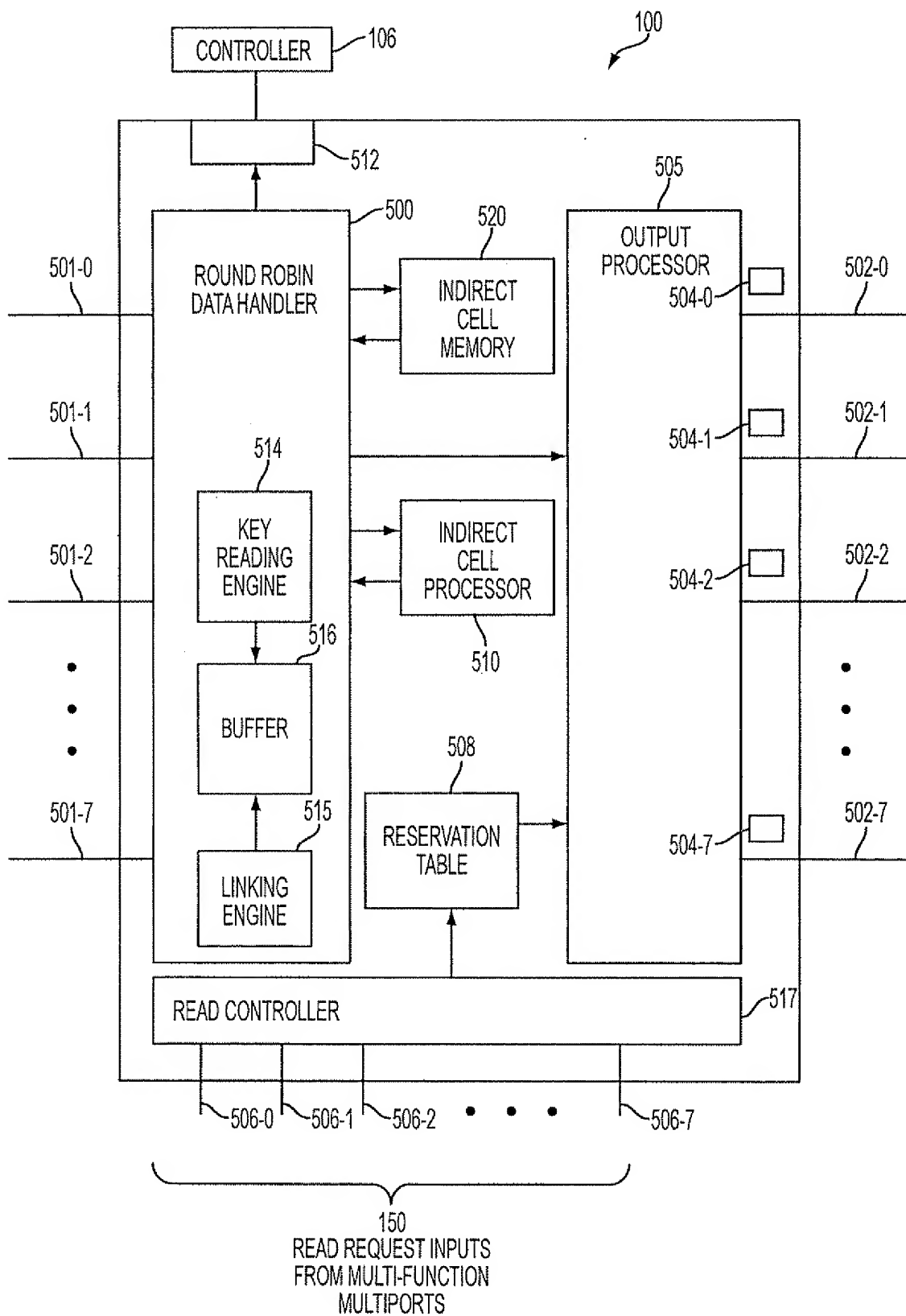


FIG. 5

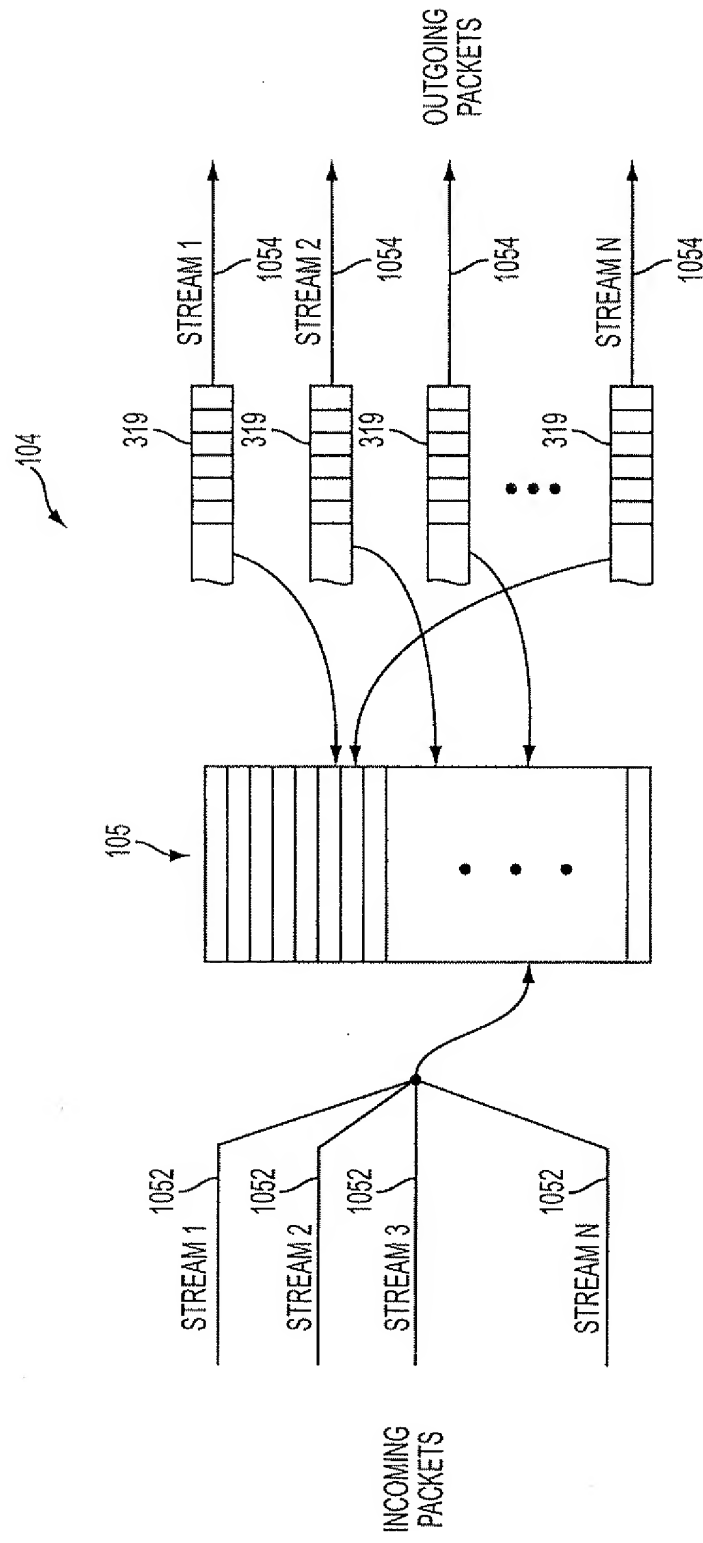


FIG. 6

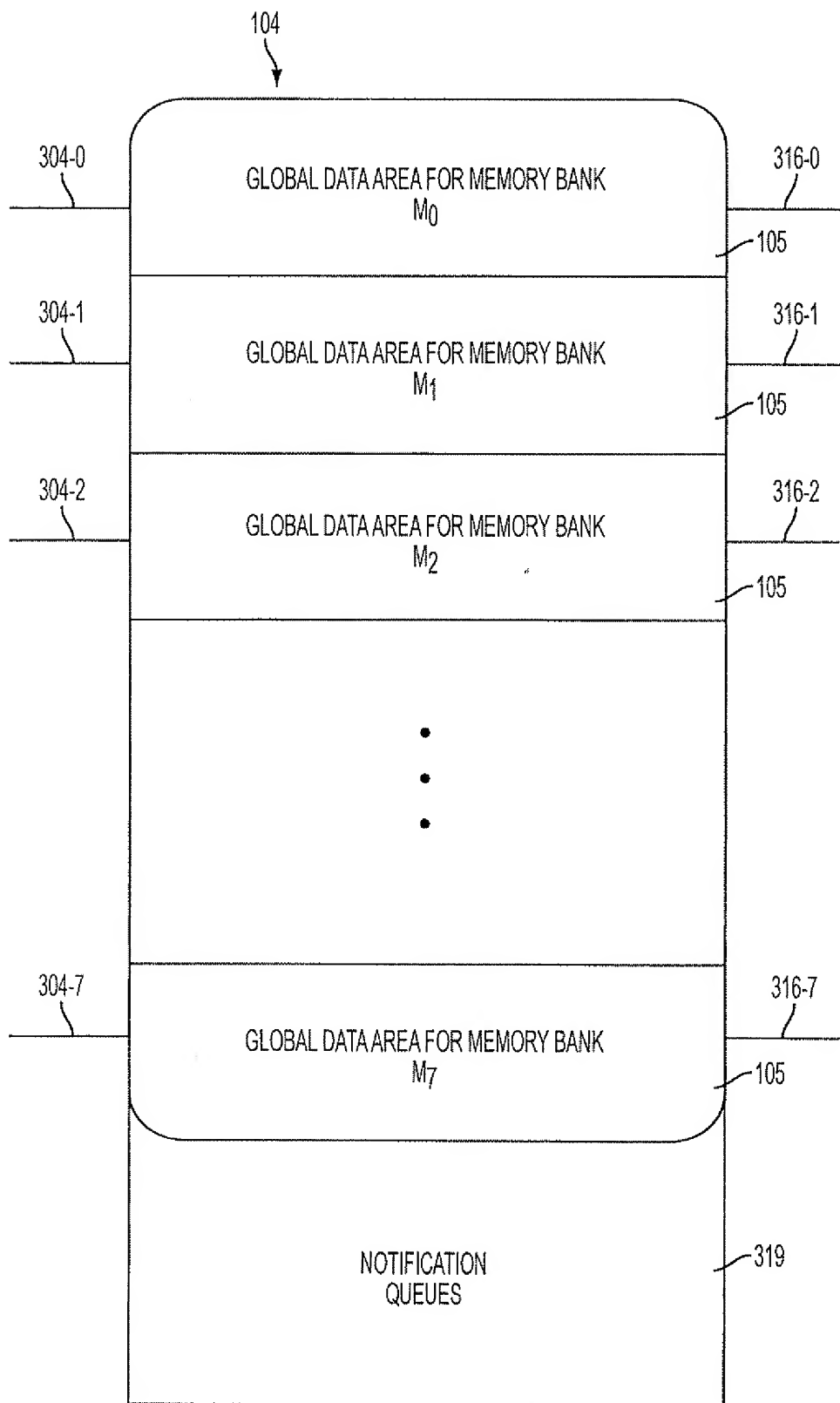


FIG. 7

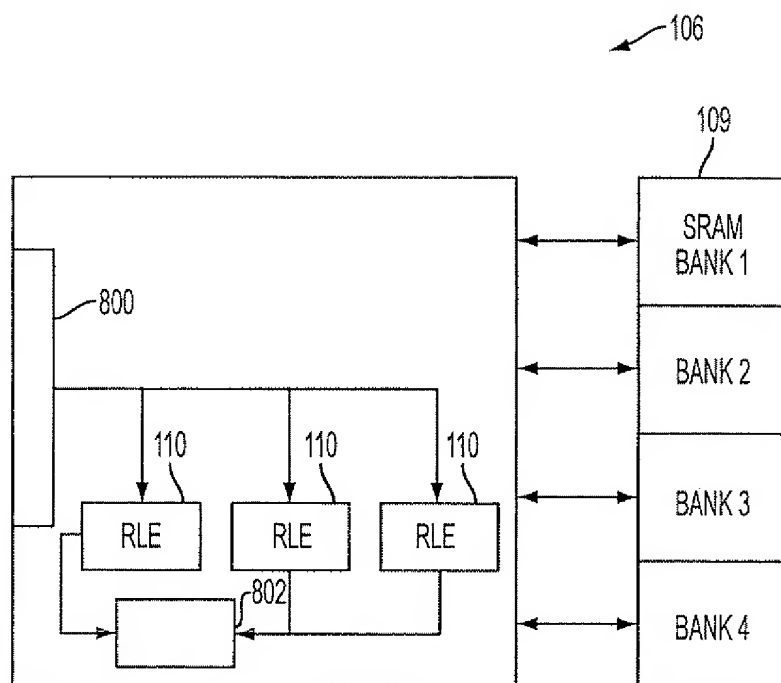


FIG. 8

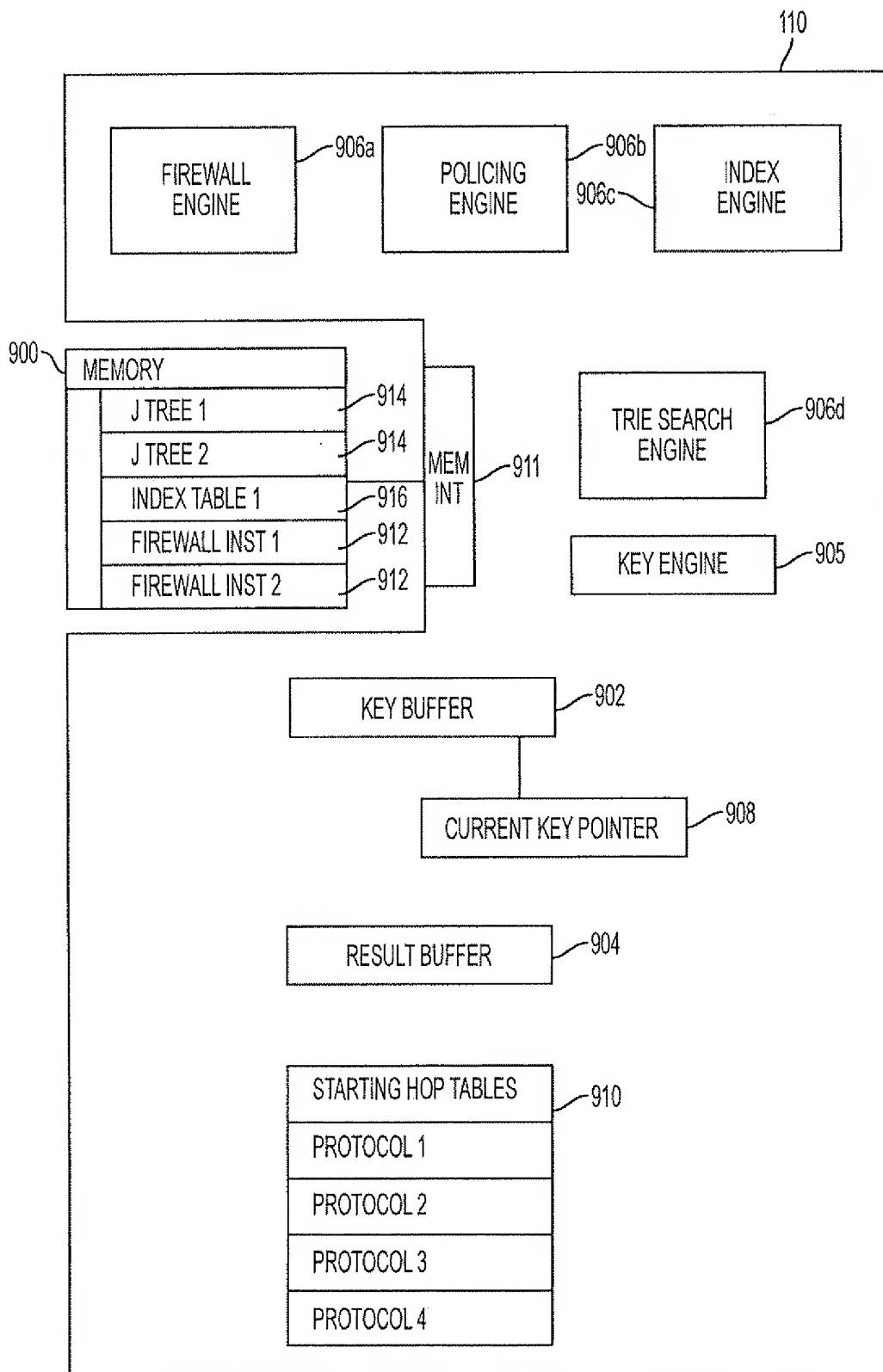


FIG. 9

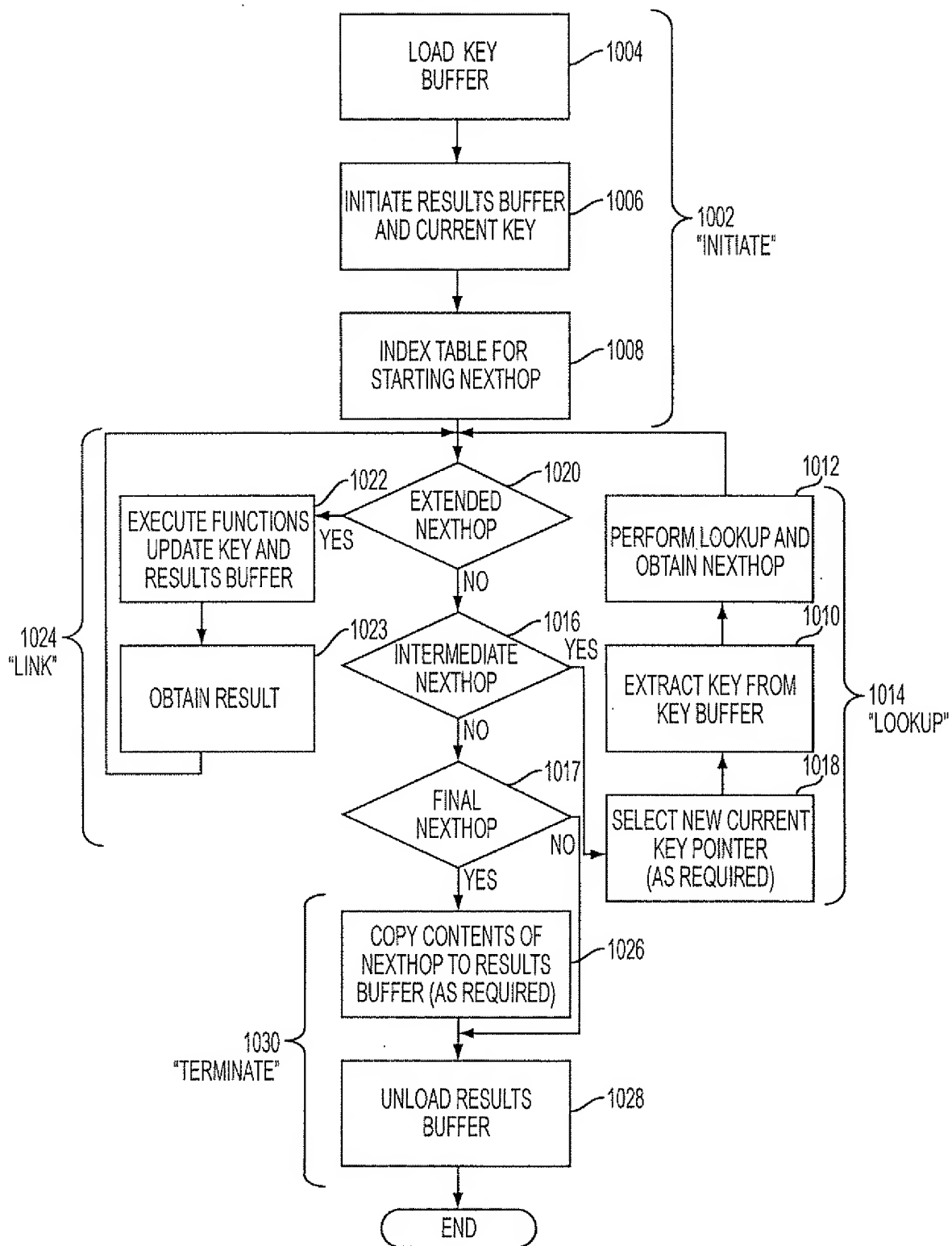


FIG. 10

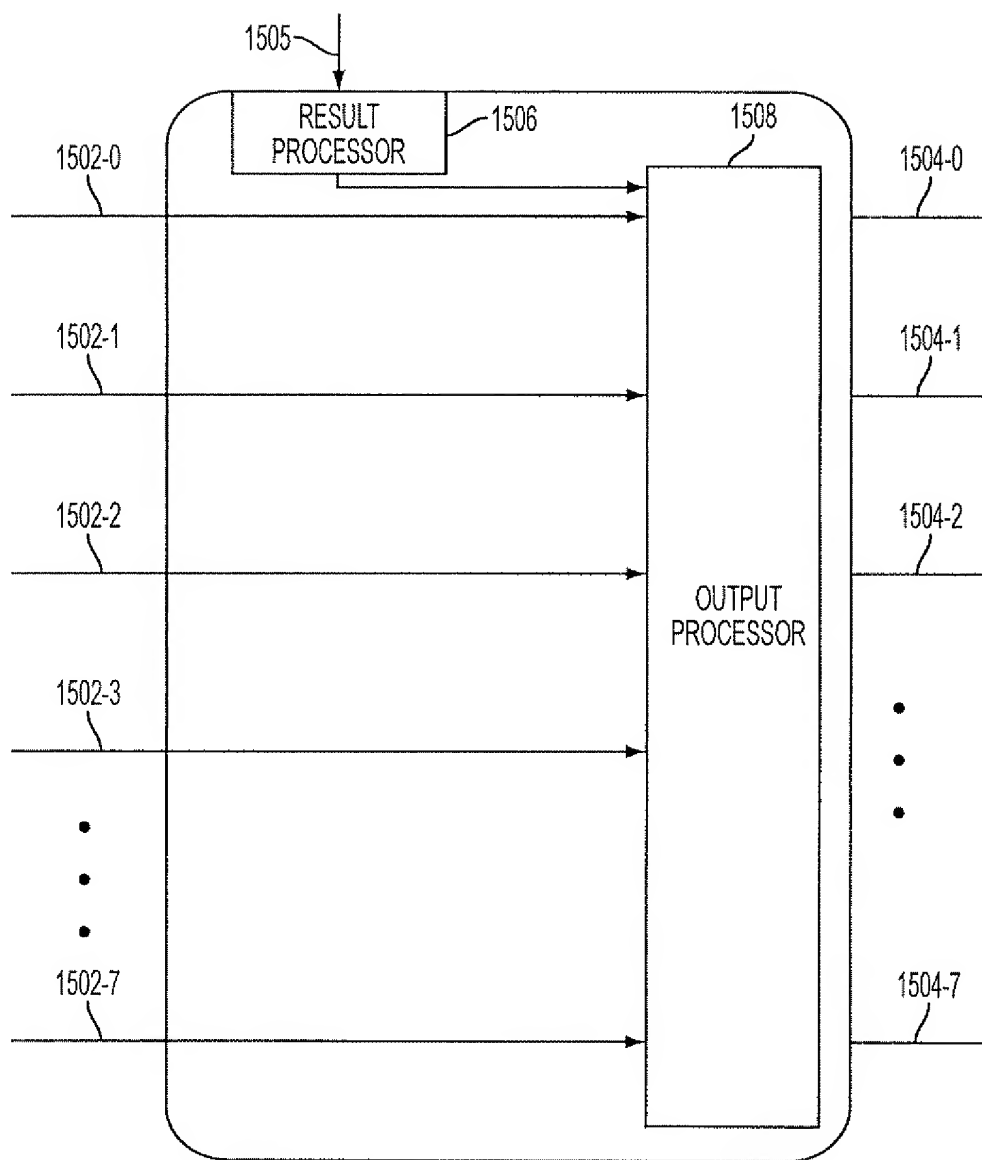


FIG. 11

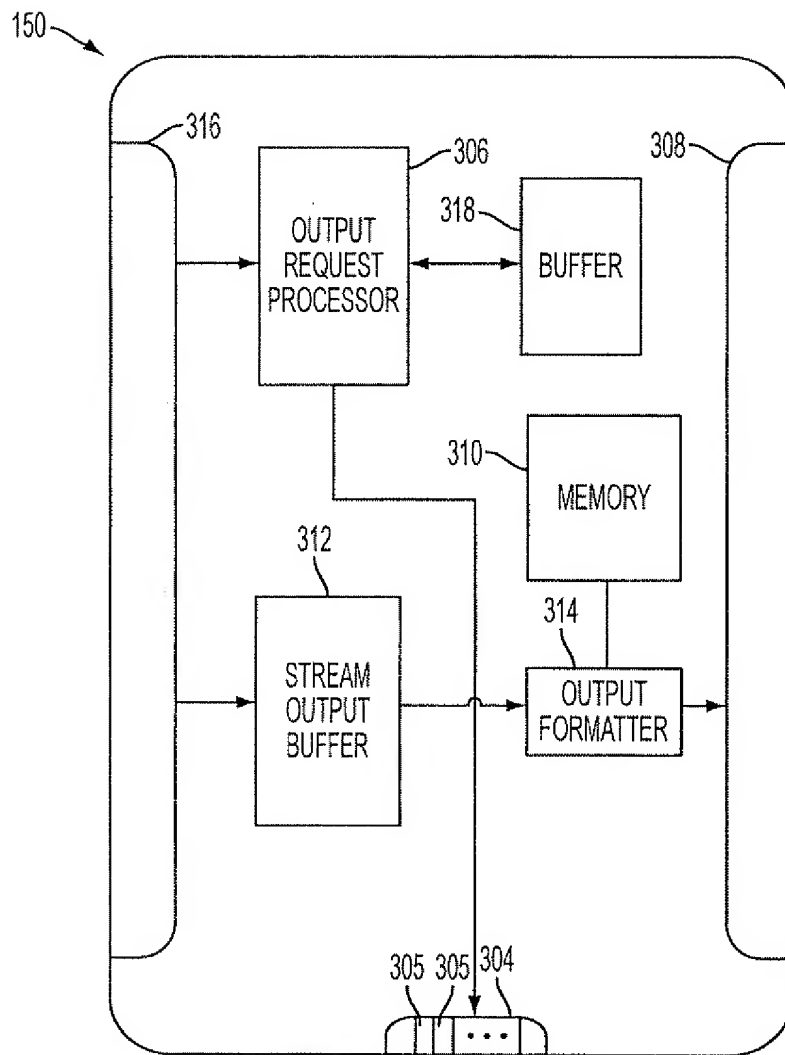


FIG. 12

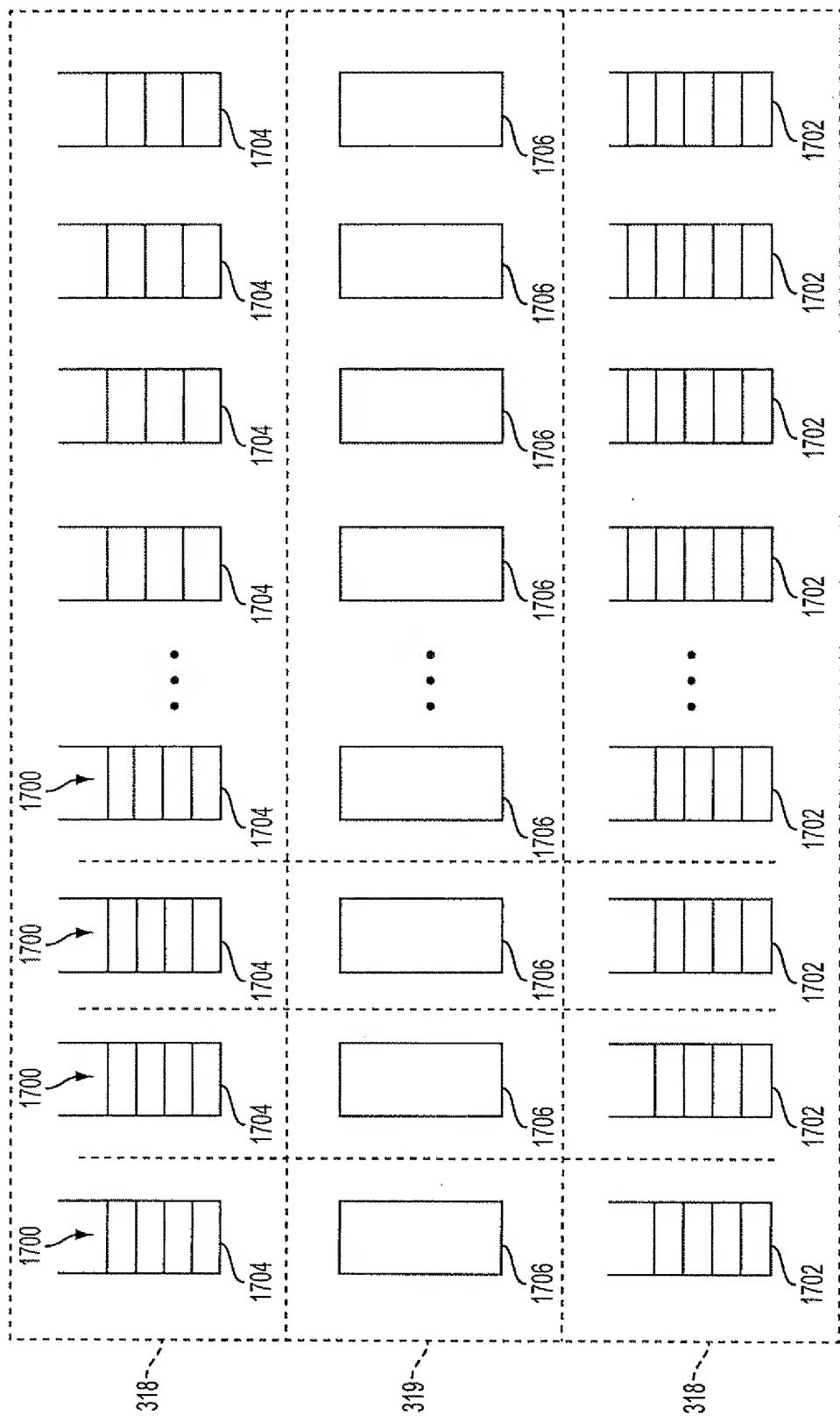


FIG. 13

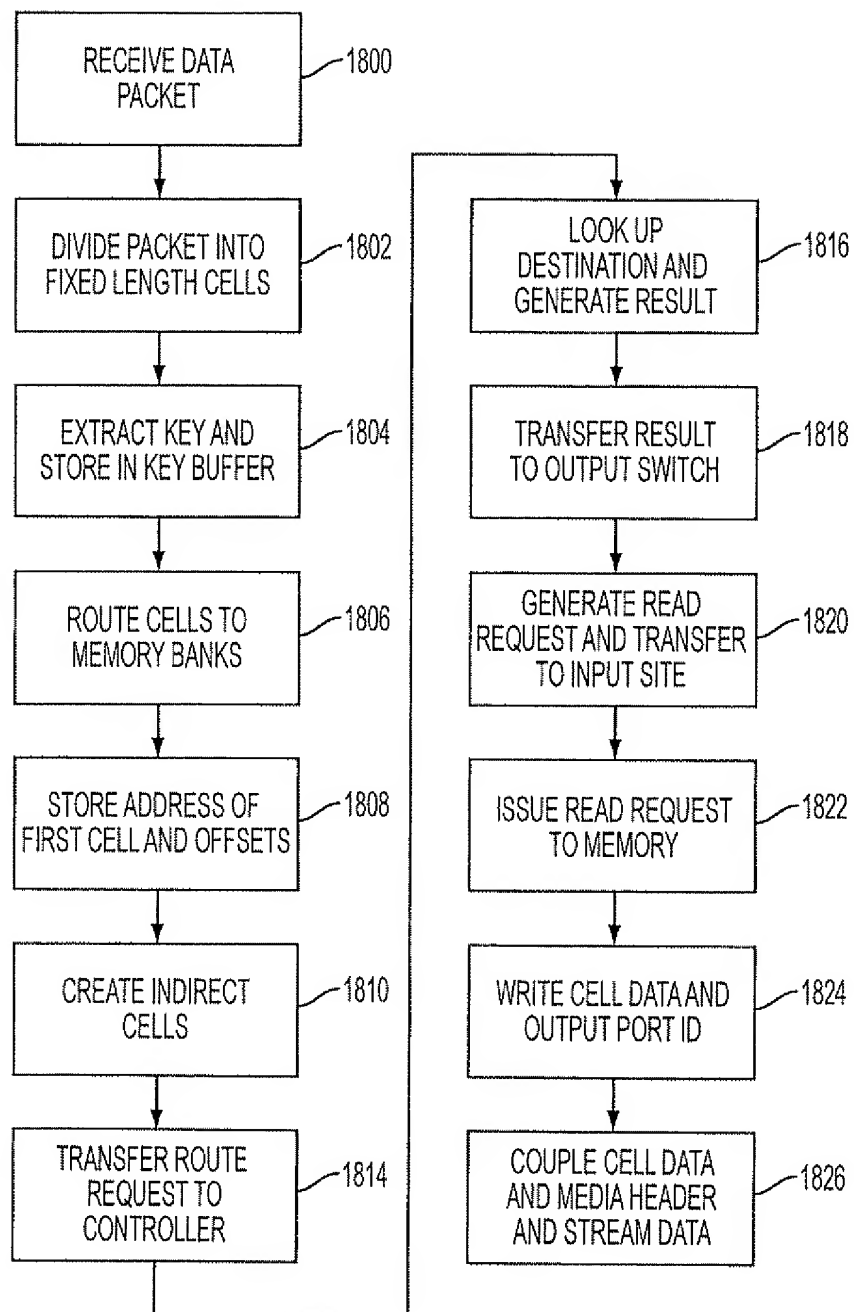


FIG. 14

# INTERNET PROCESSOR II ASIC VERSUS CPU-BASED BACKBONE ROUTER

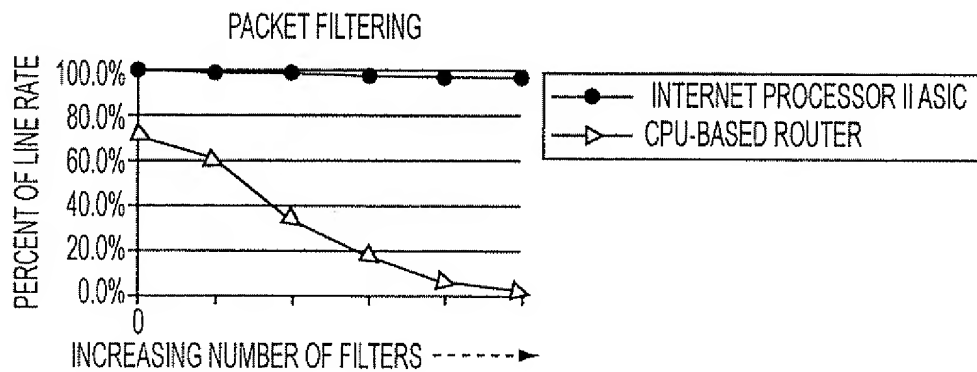


FIG. 15

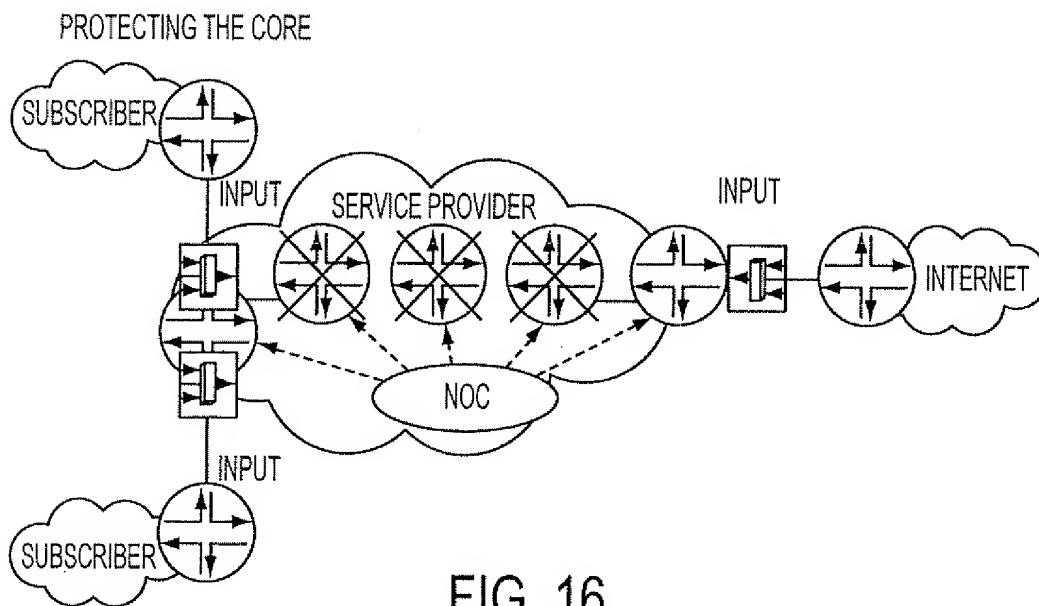


FIG. 16

# OUTSOURCE PACKET FILTERING

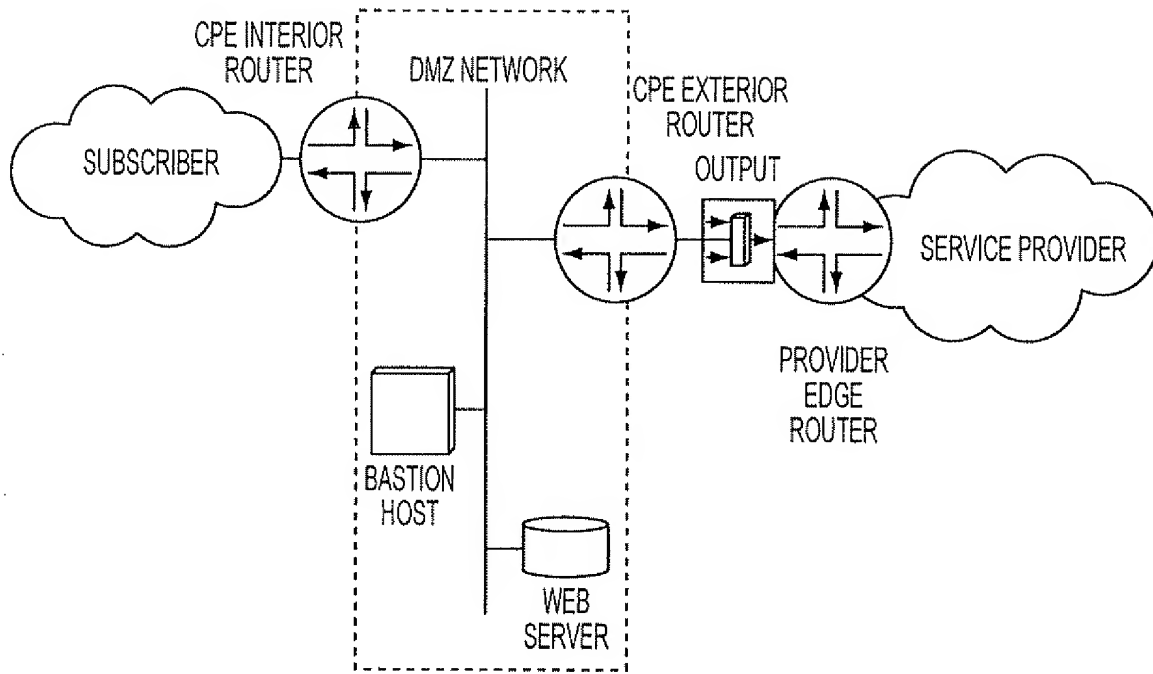


FIG. 17

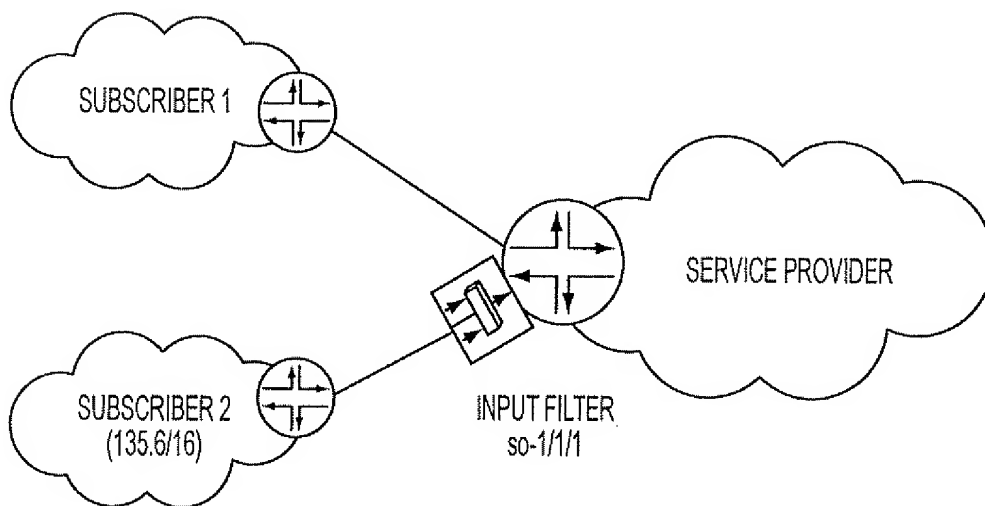


FIG. 18

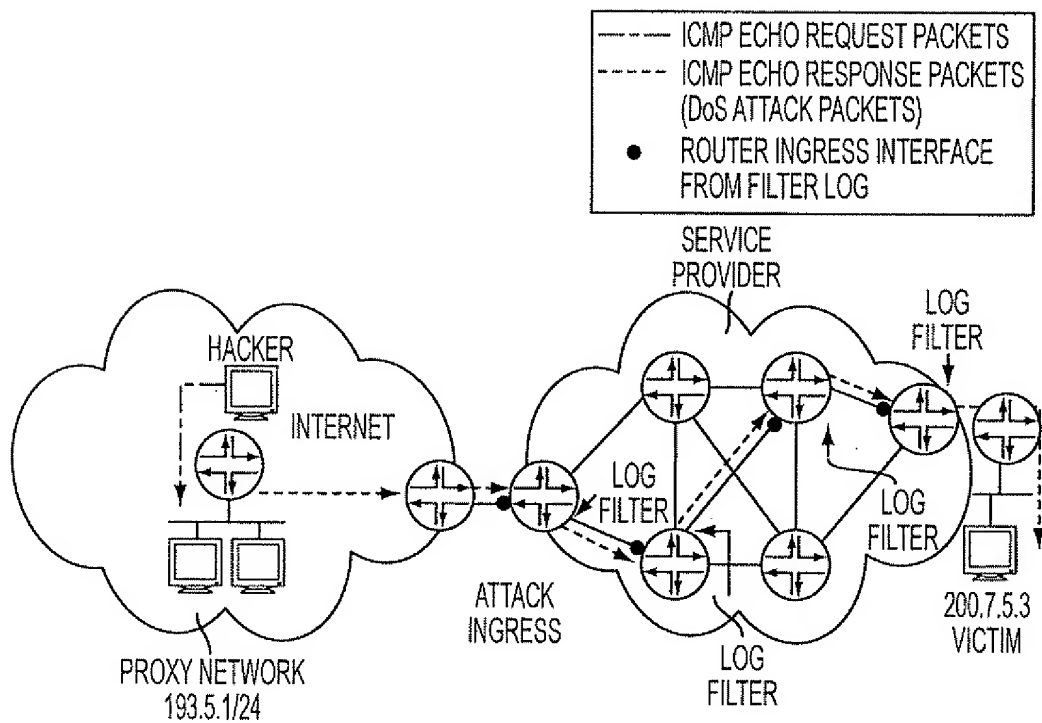


FIG. 19

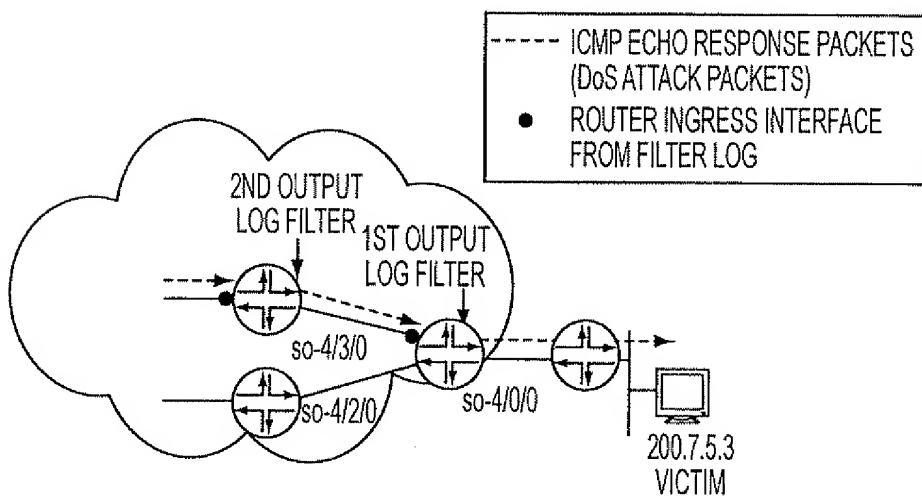


FIG. 20

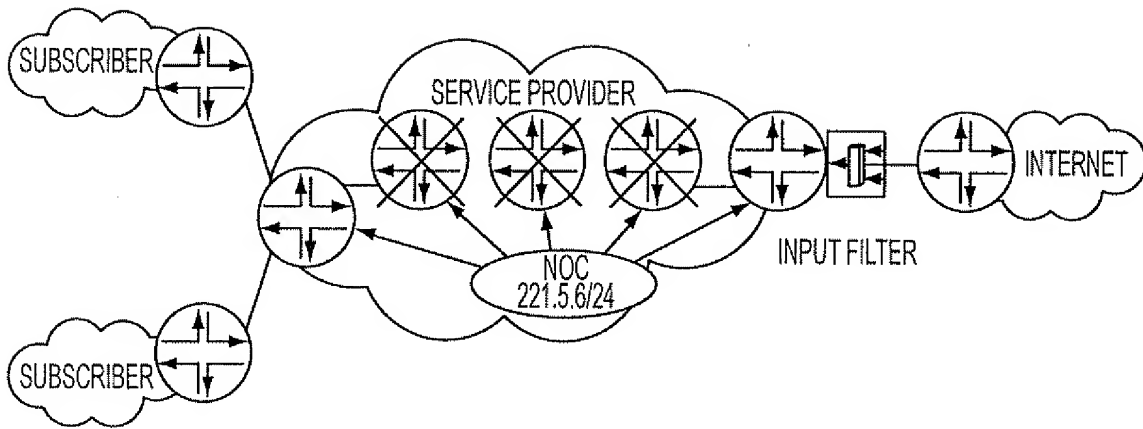


FIG. 21

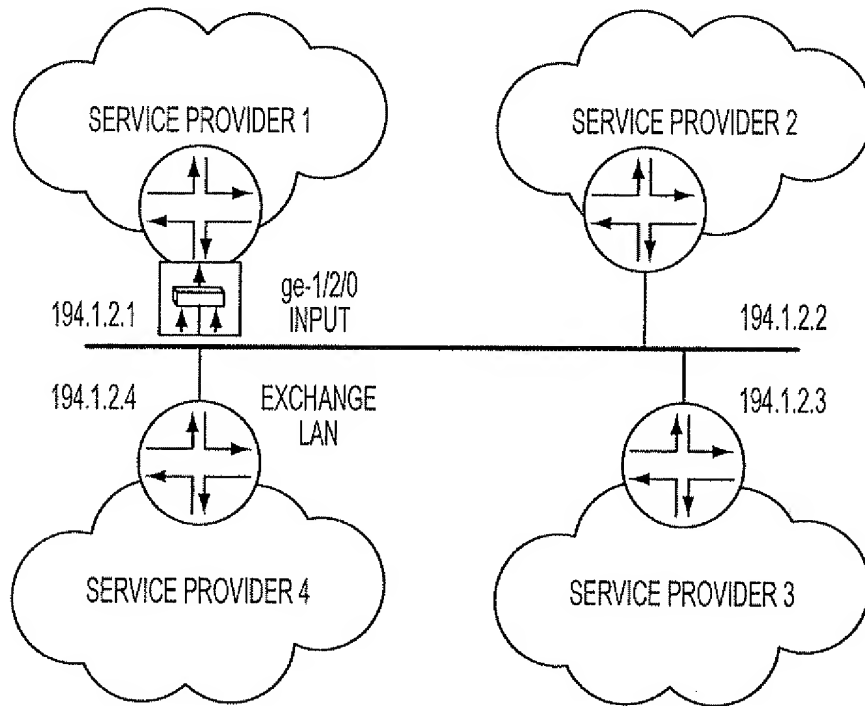


FIG. 22

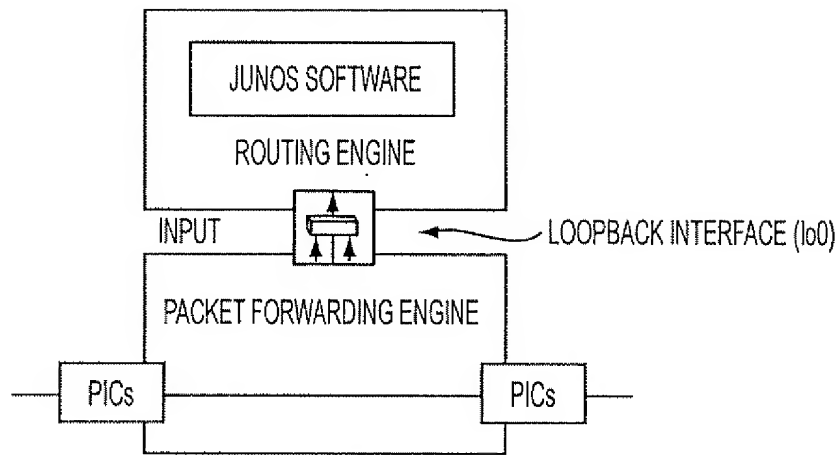


FIG. 23

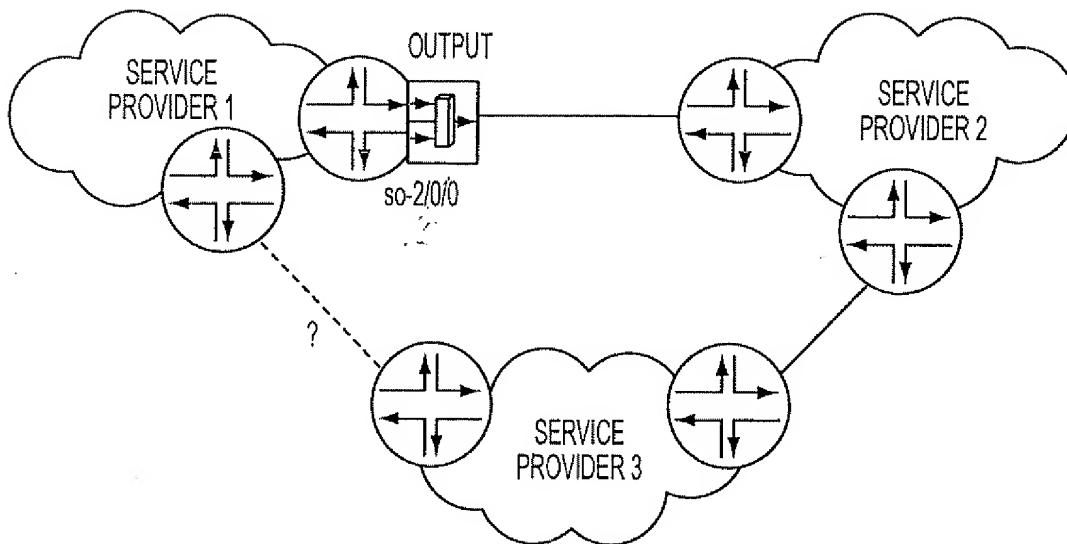


FIG. 24

# PACKET SAMPLING AND LOGGING

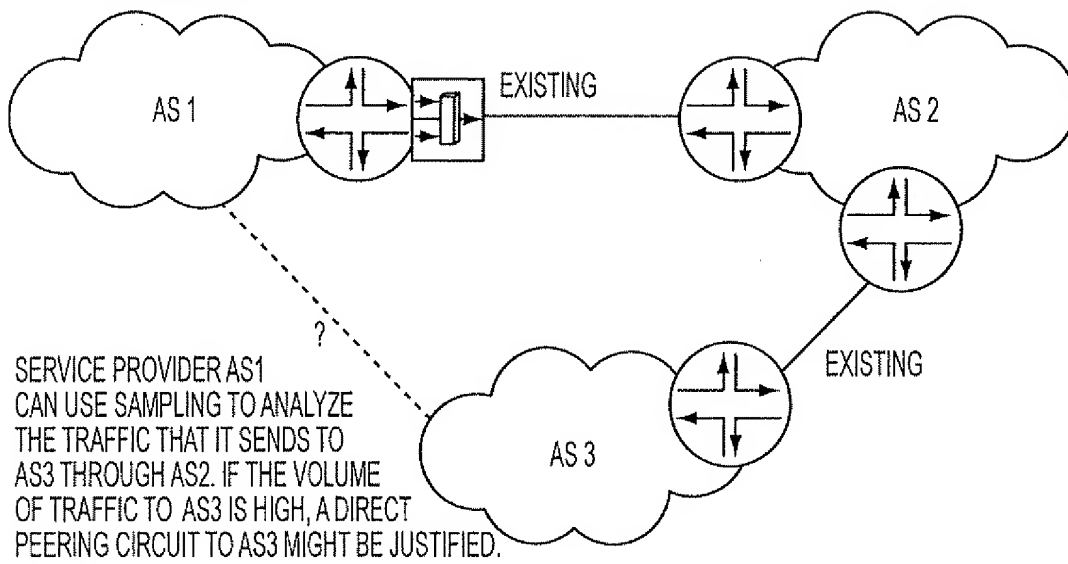


FIG. 25

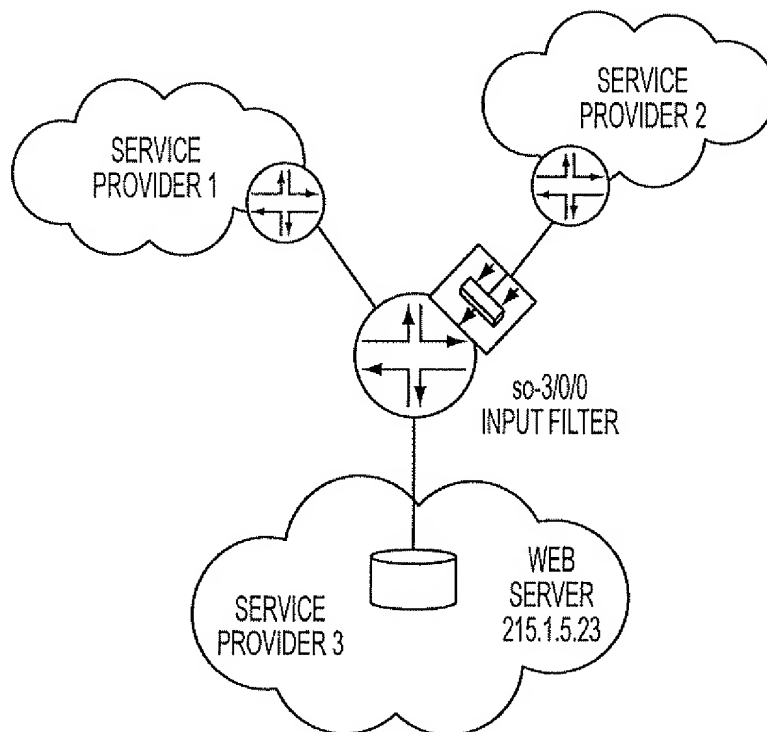


FIG. 26

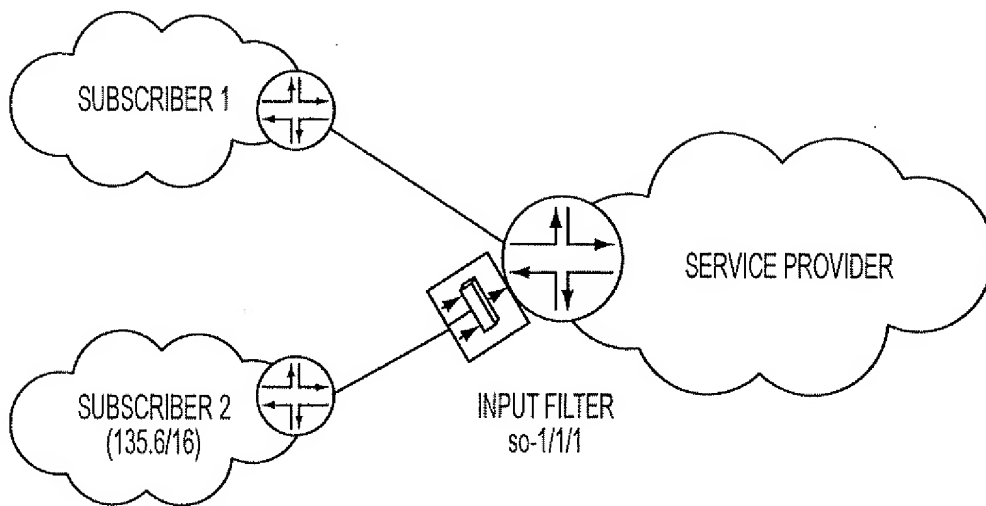


FIG. 27

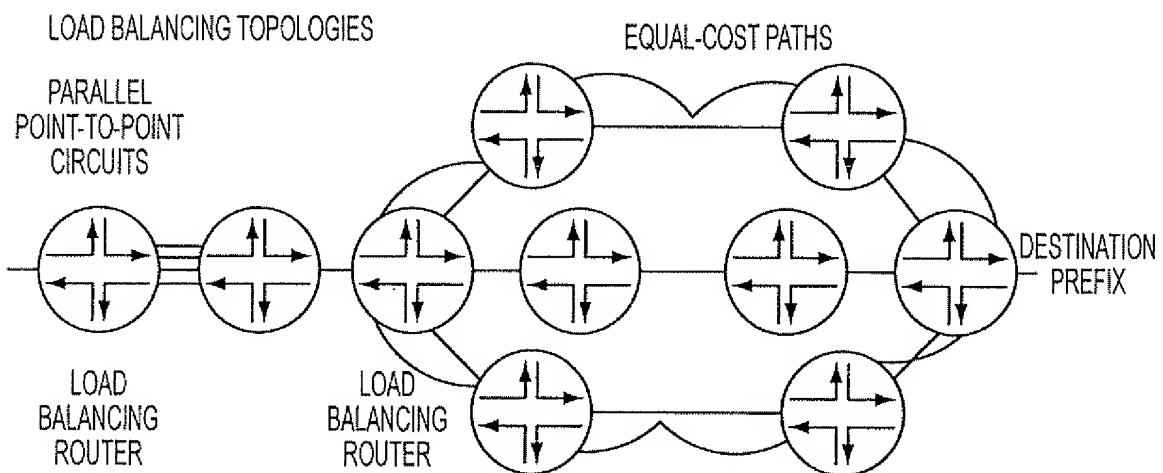


FIG. 28

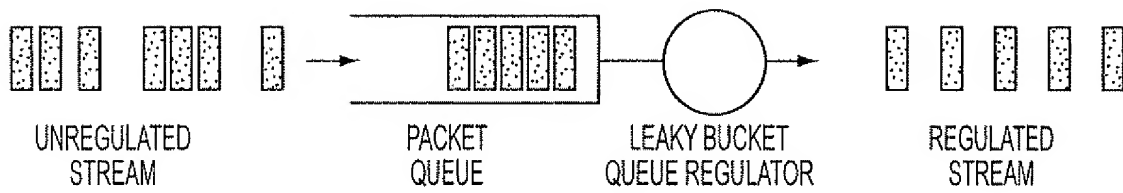


FIG. 29

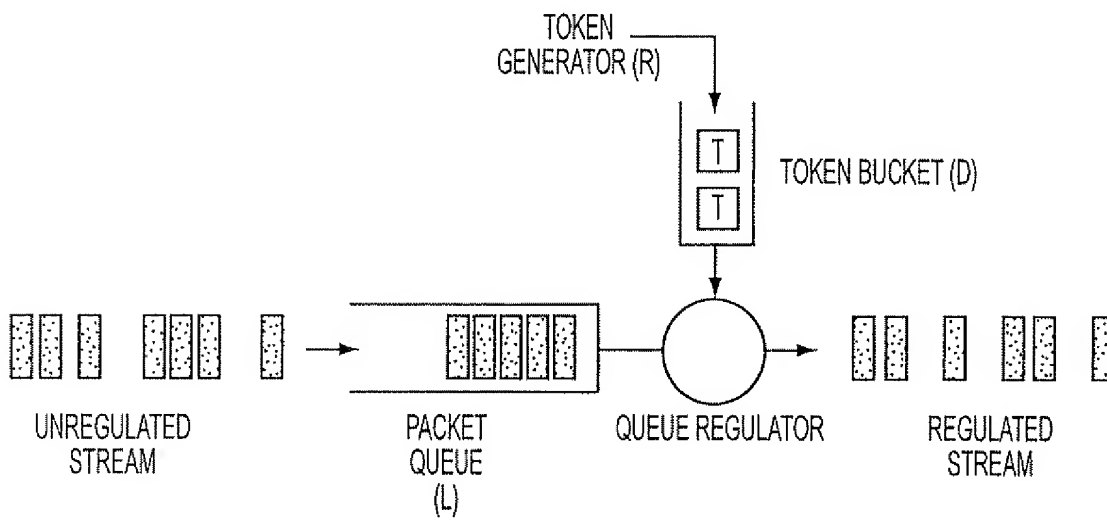


FIG. 30

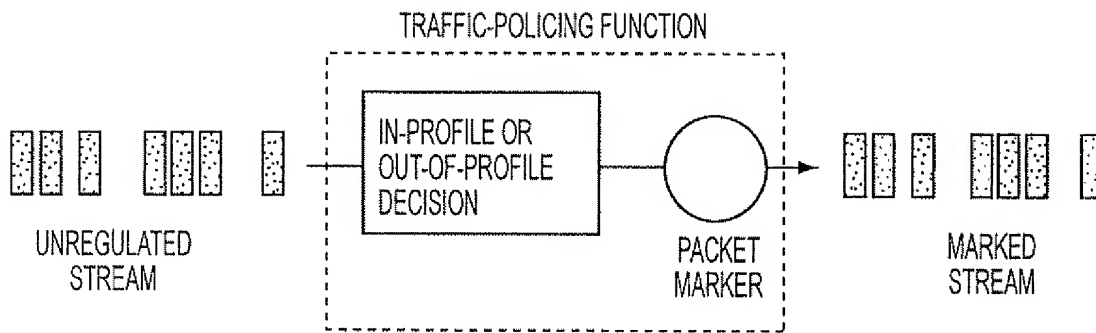


FIG. 31

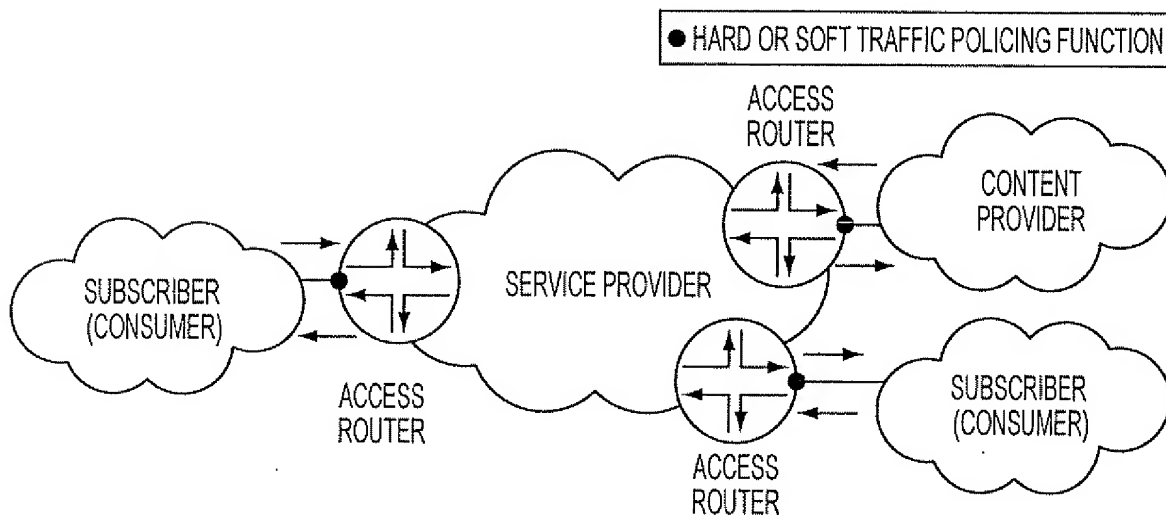


FIG. 32

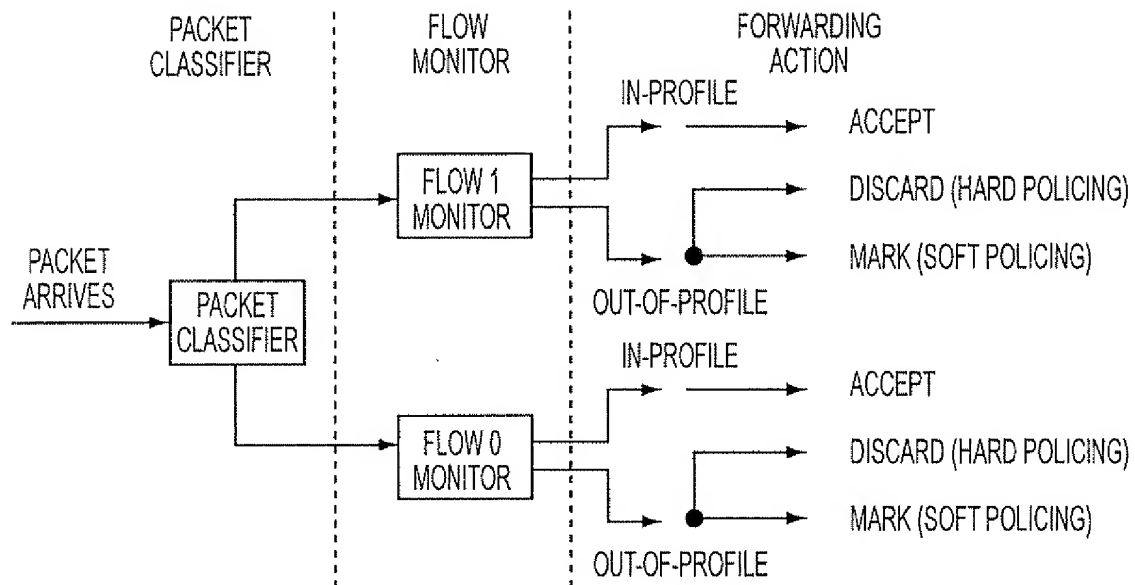


FIG. 33

RULE	IP DESTINATION ADDRESS	DIFFSERV BYTE	PEC
2	192.100/16	EXPEDITED FORWARDING PHB (101110)	2
1	208.197.169/24	ASSURED FORWARDING 2 MEDIUM DROP PHB (010100)	1
0	*	*	0

FIG. 34

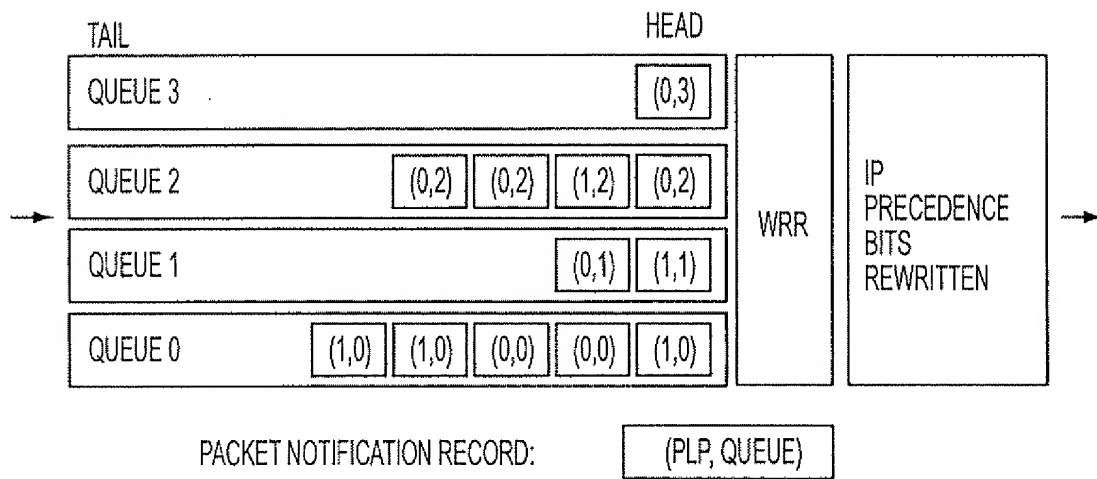


FIG. 35

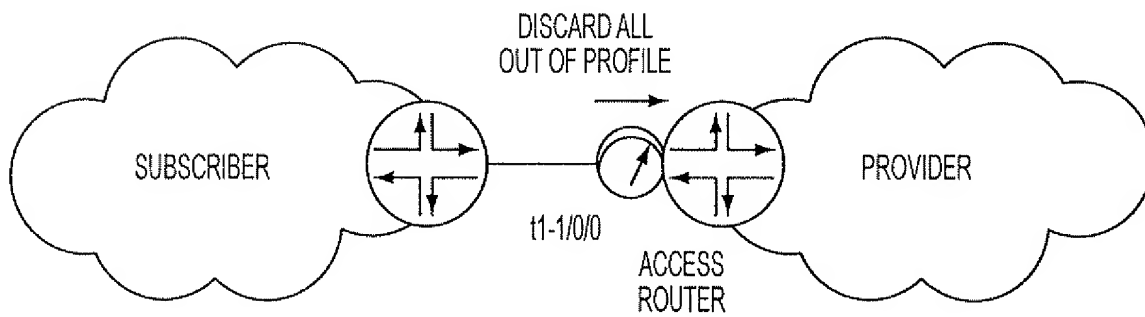


FIG. 36

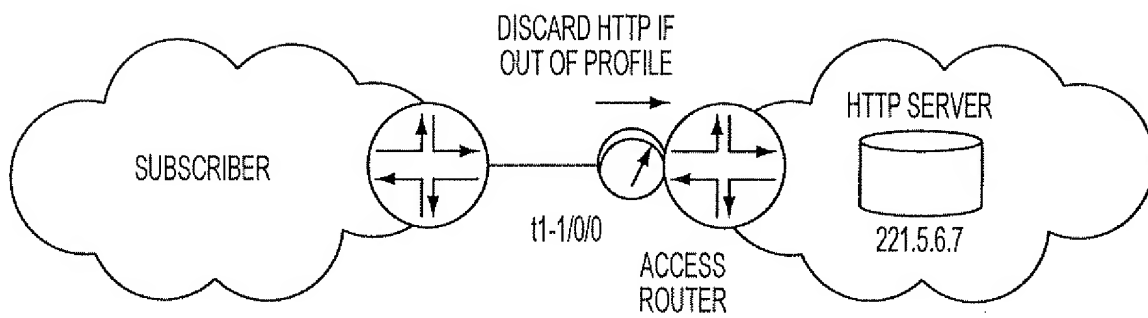


FIG. 37

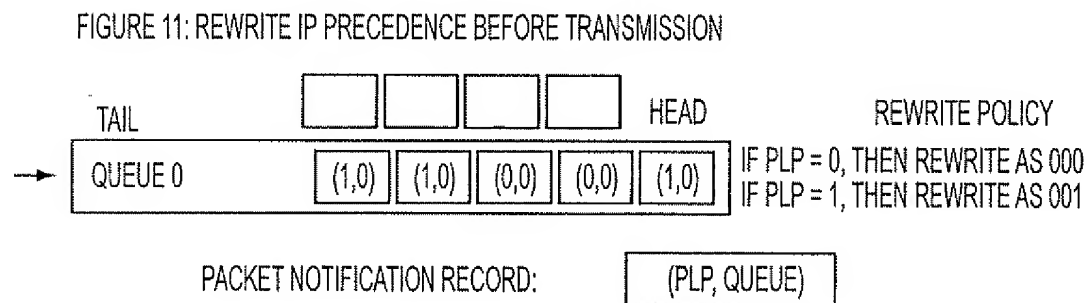


FIG. 38

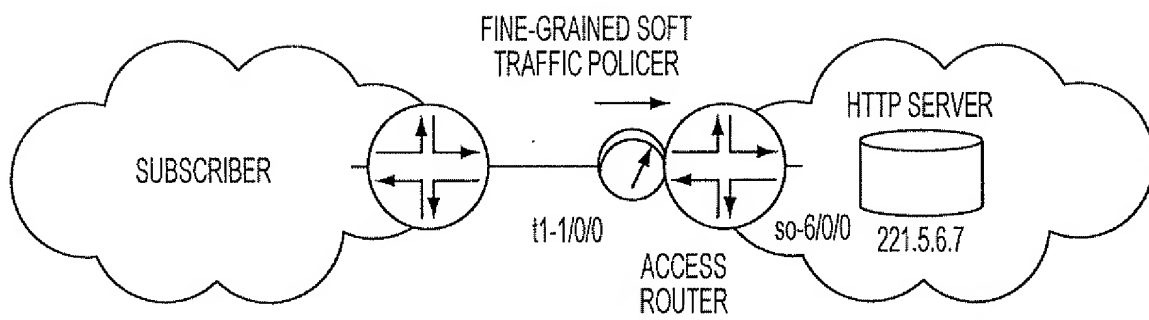


FIG. 39

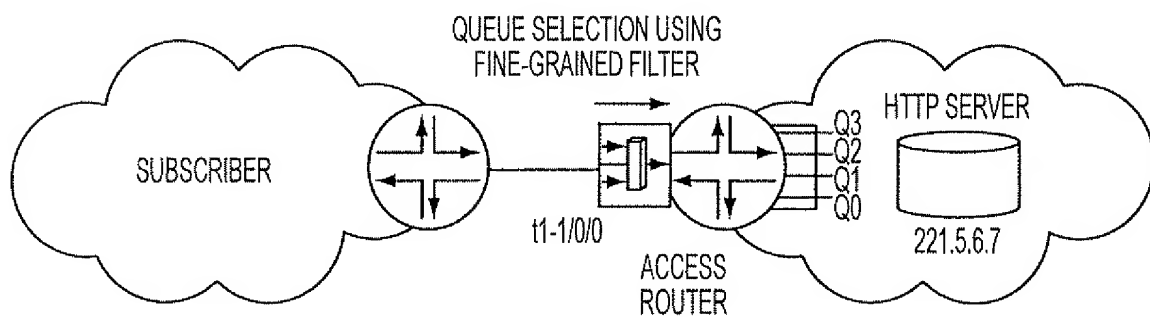


FIG. 40